

TCET/FRM/IP-02/10

Revision: B

**Semester Plan  
(Practical / Tutorials / Assignment)**

 Semester: **V**

 Course: **T.E EXTC**

 Batches: **T.E A1/A2,  
A3/A4**

Subject: Communication Engineering Lab-1

 Class: **T.E EXTC- A**

Batch size: 20 Students

 Laboratory faculty in charge: **Dr.Sangeeta Mishra .**

 Lab Assistant : **Ms Jinal Rathod**

 Note: **Experiment planned as per University Curriculum**

**Basic Experiments:**

Sr. No.	TITLES Experiments / Tutorials / Assignment (Planning with use of Technology)	Batches	Planned Date	Completi on Date	Remarks
1.	To generate & detect Double Side band Full Carrier AM wave	A1	27/7/17		
		A2	27/7/17		
		A3	26/7/17		
		A4	26/7/17		
2.	To generate & detect Frequency Modulation.	A1	3/8/17		
		A2	3/8/17		
		A3	2/8/17		
		A4	2/8/17		
3.	Study of Super heterodyne Receiver	A1	10/8/17		
		A2	10/8/17		
		A3	9/8/17		
		A4	9/8/17		
4.	Study of Sample & Hold Circuit	A1	24/8/17		
		A2	24/8/17		
		A3	16/8/17		
		A4	16/8/17		
5	To generate & detect PAM, PWM & PPM.	A1	31/8/17		
		A2	31/8/17		
		A3	30/8/17		
		A4	30/8/17		
6.	Study of Sampling Theorem	A1	7/9/17		
		A2	7/9/17		
		A3	6/9/17		
		A4	6/9/17		

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7.	To design FM generation circuit using Matlab Simulink.	A1	14/9/17		
		A2	14/9/17		
		A3	13/9/17		
		A4	13/9/17		
8.	To design DSBFC using Matlab Simulink	A1	21/9/17		
		A2	21/9/17		
		A3	20/9/17		
		A4	20/9/17		
9.	To design preemphasis and deemphasis circuits	A1	5/10/17		
		A2	5/10/17		
		A3	4/10/17		
		A4	4/10/17		
<b>Group Learning Activity:</b>					
10	Mini Project: Design of Pulse Width Modulator using IC555.	A1	12/10/17		
		A2	12/10/17		
		A3	4/10/17		
		A4	4/10/17		
11	Case Study [Modulation(CW/Pulse)]	As per slot given	As per slot given		
1. <b>Mini /Minor Projects Objective:</b> To get hands on experience to execute projects with respect to student choice in the following areas. <b>(30 Hrs / Semester / Student).</b> <b>(Total 120 Hrs)</b> <b>The areas are :</b> 1. <b>Research</b> 2. <b>Core</b> 3. <b>Interdisciplinary</b> 4. <b>Application</b> <b>Mini/ Major project :</b> As per University Scheme					
S.No	Project Title/Group Size	Class	Type / Project Hours	Modes of Learning	Reference
1.	RF based wireless remote control switch	B.E EXTC	Application	Project Based Learning	<a href="http://www.electroschematics.com/8712/rf-based-wireless-remote-control-system/">http://www.electroschematics.com/8712/rf-based-wireless-remote-control-system/</a>
2.	IR Transmitter & Receiver	B.E EXTC	Application	Project Based Learning	<a href="http://www.jpier.org/PIER/pier49/11.0403161.Chan.CC.">http://www.jpier.org/PIER/pier49/11.0403161.Chan.CC.</a>
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*Laxmi Singh Charitable Trust's (Regd.)*

# THAKUR COLLEGE OF ENGINEERING & TECHNOLOGY

(Approved by AICTE, Govt. of Maharashtra & Affiliated to University of Mumbai\*)  
(Accredited Programmes by National Board of Accreditation, New Delhi\*\*)

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ISO 9001 : 2008 Certified

\*Permanent Affiliated UG Programmes :- Computer Engineering • Electronics & Telecommunication Engineering • Information Technology (w.e.f. A.Y. 2015-16 onwards)

\*\*1st time Accredited UG Programmes :- Computer Engineering • Electronics & Telecommunication Engineering • Information Technology

\*\*2nd time Accredited UG Programmes :- Computer Engineering • Electronics & Telecommunication Engineering • Information Technology • Electronics Engineering (3 years w.e.f. 01-07-2016)

	Planned	Completed		Planned	Completed		Planned	Completed
No. of Prac	Basic Exp: 06 Design Base Exp: 03 Group Learning : 2 Major Project: 02 Mini Project: 01		No. of Assignments	02		No. of Tutorial	00	--

DOSLNE:

DOSLE (engaged in some other dates):

Group activities are required to be added with the practical related to course to enhance the learning activity of the student in the course. Group activity includes: Group presentation, new experiment design, mini projects etc.

## Note:

1. The practical plan date and completion date shall be in compliance. For any non-compliance reason(s) required to be stated in remark column.
2. Learning objective and outcome shall be clearly stated with each of experiments/ tutorials/ assignments and are required to be mapped at the end of the semester.
3. Entry for DOSLE (engaged on some other date) shall be done with proper mapping to DOSLNE.

Sd.

(Mrs Rashmita Kumari Mohapatra)

Name & Signature of Faculty

Date: 19/07/2017

Sd.

(Dr. Vinitkumar Dongre)

Signature of HOD

Date: 19/07/2017

Sd.

(Dr. R. R. Sedamkar)

Signature of Principal / Dean Academic

Date: 19/07/2017

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